

EXAMINER'S AMENDMENT

1. An Examiner's Amendment to the record appears below.

Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 C.F.R.

1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the Issue Fee.

2. The drawing filed on January 11, 2006 has been amended as follows:

In Fig. 6, a label --PRIOR ART-- has been added based on the specification, section 0032 where Fig. 6 is described as a conventional block diagram of a recording clock generation apparatus.

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Allowable Subject Matter

3. Claims 1-7 are allowable over prior art.

4. The following is an Examiner's statement of reasons for the indication of allowable subject matter based on Amendment filed on March 8, 2010:

As in claim 1, the prior art of record fails to teach or fairly suggest a recording clock generation apparatus, comprising the following features:

(a) a frequency conversion circuit for converting an inputted 32T-cycle binarized wobble signal based on a DVD+RW/+R standard into a 186T-cycle binarized wobble signal based on a DVD- R/RW standard;

(b) a selector for selecting, as a selected 186T-cycle binarized wobble signal, either the converted 186T-cycle binarized wobble signal that is outputted from the frequency conversion circuit or an inputted 186T-cycle binarized wobble signal, and outputting the selected 186T-cycle binarized wobble signal; and

(c) a PLL circuit for 186-multiplying the frequency of the selected 186T-cycle binarized wobble signal of outputted from the selector.

As in claim 2, the prior art of record fails to teach or

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fairly suggest a recording clock generation apparatus, comprising the following features:

(a) a frequency conversion circuit for converting an inputted 186T-cycle binarized wobble signal based on a DVD-R/RW standard into a 32T-cycle binarized wobble signal based on a DVD+RW/+R standard;

(b) a selector for selecting, as a selected 32T-cycle binarized wobble signal, either the converted 32T-cycle binarized wobble signal that is outputted from the frequency conversion circuit or an inputted 32T-cycle binarized wobble signal, and outputting the selected 32T-cycle binarized wobble signal; and

(c) a PLL circuit for 32-multiplying the frequency of the selected 32T-cycle binarized wobble signal outputted from the selector.

As in claim 7, the prior art of record fails to teach or fairly suggest a recording clock generation apparatus, comprising the following features:

(a) a frequency conversion circuit for converting an inputted first cycle binarized wobble signal based on a first optical disc standard into a second cycle binarized wobble signal based on a second optical disc standard;

(b) a selector for selecting, as a selected second cycle

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binarized wobble signal, either the second cycle binarized wobble signal that is outputted from the frequency conversion circuit or an inputted second cycle binarized wobble signal, and outputting the selected second cycle binarized wobble signal; and

(c) a PLL circuit for multiplying the frequency of the selected second cycle binarized wobble signal outputted from the selector to change its cycle from the wobble cycle to the cycle of the recording clock.

The features indicated above, in combination with the other elements of the claims, are not anticipated by, nor made obvious over, the prior art of record.

5. Any comments considered necessary by applicant must be submitted no later than the payment of the Issue Fee and, to avoid processing delays, should preferably accompany the Issue Fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

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6. Any inquiry concerning this communication or earlier communication from the examiner should be directed to Kim CHU whose telephone number is (571) 272-7585 between 9:30 am to 6:00 pm, Monday to Friday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hoa Nguyen, can be reached on (571) 272-7579.

The fax number for the organization where this application or proceeding is assigned is (571) 273-8300

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished application is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9191 (toll free).

/Kim-Kwok CHU/
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June 1, 2010
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